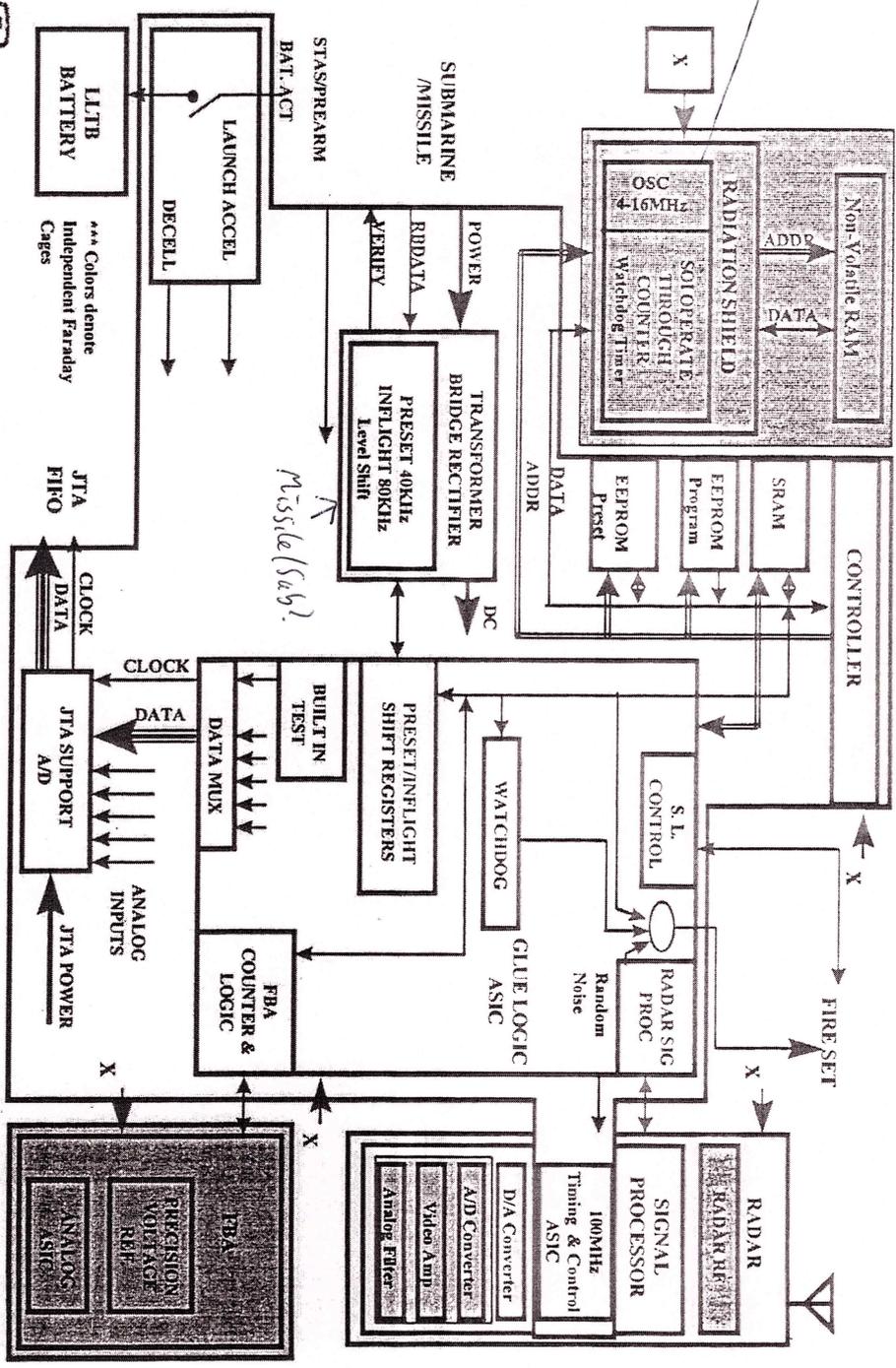


A&F architecture to support W76/MK4 and Pit Reuse



CCTB

Sandia National Laboratories

First battery to be activated
FIFO - First in First out
Recalls - AC-APC

STAS - Safe to Arm Signal
STAS type and signals set & read RB before deployment

Precision Voltage Reference

UNCLASSIFIED

UNCLASSIFIED

SOI - Silicon on Insulator

→ Read

bridge rectifier - used for AC-APC

EEPROM - ~~Electro~~ Erasable Programmable Read Only Memory
- Non-volatile Memory
A&F - Address
SC - System (size?)

Share logic - circuit to interface between several I.C.'s

Modulo time - reads system if Z fails

A/D - Analog to Digital

EBA
Voltage varies with accident but not FBR1A